



University Program Software Selection

Americas University Program 2014

Custom Integrated Circuits Bundle

The Custom IC Bundle Software Reference List:

<u>Product</u>	<u>Product No.</u>	<u>Release Stream</u>
Design Environment		
Virtuoso [®] Simulation Environment	206	IC616
Virtuoso [®] AMS Designer Environment	70000	IC616
Virtuoso [®] Analog Design Environment - XL	95210	IC616
Virtuoso [®] Analog Design Environment - GXL	95220	IC616
Virtuoso [®] Implementation Aware Design Option	95510	IC616
Virtuoso [®] Behavioral Modeling Option	70040	IC616
Virtuoso [®] SMG Runtime ? 10 Pack	70050	IC616
Design Entry		
Cadence [®] SKILL Development Environment	900	IC616
Virtuoso [®] Schematic VHDL Interface	21060	IC616
Virtuoso [®] Schematic Editor Verilog ^R Interface	21400	IC616
Virtuoso [®] Schematic Editor - XL	95115	IC616
Virtuoso [®] Analog Oasis Run-Time Option	32100	IC616
Circuit Simulation		
Virtuoso [®] Schematic Editor HSPICE Interface ¹	276	IC616
Virtuoso [®] Analog HSPICE Interface Option ¹	32760	IC616
Virtuoso [®] Multi-mode Simulation Power Option	91400	MMSIM121
Virtuoso [®] Multi-mode Simulation CPU Accelerator option	91500	MMSIM121
Virtuoso [®] RelXpert	33580	MMSIM121
Virtuoso [®] Multi-mode Simulation with AP Simulator	90003	MMSIM121
Spectre Extensive Partitioned Simulator	91600	MMSIM121
Spectre Characterization Simulator Option	3500	MMSIM121
Interfaces		
Virtuoso [®] EDIF 200 Reader	940	IC616
Virtuoso [®] EDIF 200 Writer	945	IC616
Cadence [®] Design Framework Integrator's Toolkit	12141	IC616

¹ requires additional licenses that must be obtained from Synopsys



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Physical Verification		
Dracula [®] Graphical User Interface	365	IC616
Dracula [®] Physical Verification and Extractor Suite	70520	IC616
Diva [®] Physical Verification and Extractor Suite	71520	IC616
Assura [™] Design Rule Checker	72110	ASSURA41
Assura [™] Layout vs. Schematic Verifier	72120	ASSURA41
Assura [™] Graphical User Interface Option	72140	ASSURA41
Assura [™] Multiprocessor Option	72150	ASSURA41
Virtuoso [®] QRC Extraction - XL	QRCX300	PVE121
Virtuoso [®] Advanced Analysis GXL option	QRCX310	PVE121
Cadence [®] QRC Display Technology GXL Option	QRCX330	PVE121
Cadence [®] QRC Advanced Modeling20 GXL Option	QRCX520	PVE121
Cadence [®] QuickView Layout and Mask Data Viewer	K2200	PVE121
Cadence [®] QuickView Sing-off Data Analysis Environment	K2211	PVE121
Cadence [®] Physical Verification System Design Rule Checker XL	96210	PVE121
Cadence [®] Physical Verification System Layout vs. Schematic Checker XL	96220	PVE121
Cadence [®] Physical Verification System Results Manager	96240	PVE121
Cadence [®] Physical Verification System graphic LVS Debugger	96250	PVE121
Cadence [®] Physical Verification System Constraint Validator	96300	PVE121
Cadence [®] Physical Verification System Advanced Device Parameter Extraction	96301	PVE121
Cadence [®] Physical Verification System Advanced Analysis Option	96320	PVE121
Virtuoso [®] Integrated Physical Verification System Option for Layout Suite	96400	PVE121
Pcell Generator	PASPCG	PAS31
Graphical Technology Editor	PASGTE	PAS31
Generator for Assura [™] compatible verification decks	PASASG	PAS31
Generator for Diva [®] compatible verification decks	PASDIG	PAS31
Generator for Dracula [®] compatible verification decks	PASDRG	PAS31
Error Cell Generator	PASECG	PAS31



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Digital Integrated Circuits Bundle

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<u>Product</u>	<u>Product No.</u>	<u>Release Stream</u>
Place & Route and Timing		
Virtuoso [®] Layout Suite - GXL ¹	95321	IC616
Virtuoso [®] DFM Option	95311	IC616
Cadence [®] Chip Assembly Router ²	3300	IC616
Design for Manufacturing		
Virtuoso [®] Power System – XL	VPS200	IC616
Encounter Power System - XL	EPS200	ETS120
EPS Advanced Analysis GXL Option	EPS201	ETS120
Signal Integrity		
Encounter [™] Timing System - XL	FE725	ETS120
ETS Advanced Analysis GXL Option	FE830	ETS120
Silicon Virtual Prototyping		
Encounter [™] Digital Implementation System - XL	EDS200	EDI120
Encounter [™] Low Power GXL Option	EDS10	EDI120
Encounter [™] Advanced Node GXL option	EDS30	EDI120
Encounter [™] Mixed Signal GXL Option	EDS20	EDI120
Encounter [™] DFM GXL Option	EDS50	MVS121

¹ includes CIF reader/writer and Virtuoso Stream Interface

² includes GDSII/EDIF and LEF/DEF interfaces



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The Digital IC Bundle Software Reference List, continued

Test

Architect Advanced Option to RC	ET021	ET121
Encounter TM True Time Test Advanced	ET023	ET121
Encounter TM Diagnostics Basic	ET010	ET121

Formal Verification

Encounter TM Conformal - GXL	CFM300	CONFRML121
Encounter TM Conformal Low Power - XL	CFM500	CONFRML121

Synthesis

Encounter TM RTL Compiler - XL	RC200	RC121
Encounter TM RTL Compiler Low Power Option	RC310	RC121
Encounter TM RTL Compiler Advanced Physical Option	RC340	RC121
C-to-Silicon Compiler – L	CTS102	CTOS112

Chip Planning

Cadence [®] InCyte Chip Estimator L	CPS100	CICE51
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Verification Bundle

The Verification Bundle Software Reference List

<u>Product</u>	<u>Product No.</u>	<u>Release Stream</u>
Functional Verification		
Cadence [®] Simulation Analysis Environment (SimVision)	25010	INCISIV131
Verifault – XL Simulator	26500	INCISIV131
Incisive [™] Enterprise Simulator - XL	29651	INCISIV131
Enterprise Simulator - XL Interface for MTI	29661	INCISIV131
Enterprise Simulator - XL Interface for VCS	29671	INCISIV131
Incisive [™] Formal Verifier	23560	INCISIV131
Incisive [™] Enterprise Verifier – XL	IEV101	INCISIV131
Incisive [™] Software Extensions	ISX100	INCISIV131
Virtuoso [®] AMS Designer Verification Option	70030	INCISIV131
AMS Designer with Flexible Analog Simulation	70020	INCISIV131
Digital Mixed Signal Option to IES	29710	INCISIV131
Verification Process Automation		
Incisive [™] Enterprise Manager	EMG100	INCISIV131



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Silicon-Package-Board Bundle

The Packaging & Board Bundle Software Reference List

<u>Product</u>	<u>Product No.</u>	<u>Release Stream</u>
PCB Design and Layout		
Allegro [®] PCB Designer	PA3100	SPB166
Allegro [®] PCB High-Speed Option	PA3110	SPB166
Allegro [®] PCB Miniaturization Option	PA3120	SPB166
Allegro [®] Design Authoring High-Speed Option	PA1410	SPB166
Allegro [®] PCB Routing Option	PS3500	SPB166
Allegro [®] PCB Librarian - XL	PX3500	SPB166
Allegro [®] 2 FPGA System Planner Option	PA8250	SPB166
IC Packaging		
Cadence [®] SiP Layout – XL	SIP225	SPB166
Simulation		
Allegro [®] AMS Simulator ¹	PS2200	SPB166
Sigrity Products		
Allegro [®] Sigrity SI Base	PA5700	ASI166
Allegro [®] Sigrity Package Assessment and Extraction Option	SIGR945	ASI166

¹ superset of Pspice and Advanced Analysis Option



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Changes made for this year:

Discontinued products

38500 - Virtuoso Spectre Circuit Simulator
 33400 – Virtuoso UltraSim Simulator
 38520 – Virtuoso Spectre RF Simulation with AP
 QRCX100 – Virtuoso QRC Extraction – L
 VPS100 – Virtuoso Power System – L
 EPS100 – Encounter Power System – L
 FE625 – Encounter Timing System – L
 RC110 – Encounter RTL Compiler – L
 29610 – Incisive Enterprise Simulator – L
 PX3100 – Allegro PCB SI – XL

Replacement product

90003 – Virtuoso Multi-mode Simulation Option
 90003 – Virtuoso Multi-mode Simulation Option
 90003 – Virtuoso Multi-mode Simulation Option
 QRCX300 – Virtuoso QRC Extraction – XL
 VPS200 – Virtuoso Power System – XL
 EPS200 – Encounter Power Systems – XL
 FE725 – Encounter Timing System – XL
 RC200 – Encounter RTL Compiler - XL
 29651 – Incisive Enterprise Simulator – XL
 PA5700 – Allegro Sigrity SI Base

New products added in 2014:

Spectre Characterization Simulator Option
 Spectre Extensive Partitioned Simulator
 Virtuoso® Behavioral Modeling Option
 Virtuoso® SMG Runtime ? 10 Pack
 Virtuoso® Implementation Aware Design Option
 Cadence® Physical Verification System Advanced Device
 Cadence® Physical Verification System Advanced Analysis
 Virtuoso® Integrated Physical Verification System Option
 Cadence® QuickView Sing-off Data Analysis Environment
 Cadence® QRC Advanced Modeling20 GXL Option
 Digital Mixed Signal Option to IES

Product

3500
 91600
 70040
 70050
 95510
 96301
 96320
 96400
 K2211
 QRCX520
 29710

Release

MMSIM121
 MMSIM121
 IC616
 IC616
 IC616
 PVE121
 PVE121
 PVE121
 PVE121
 PVE121
 PVE121
 INCISIV131